

#231 Letter
T. U.S.W.Y
5-3-99

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shunpei YAMAZAKI

Serial No.: 08/799,506

Filed: February 12, 1997

For: METHOD FOR PROCESSING
SEMICONDUCTOR DEVICE,
APPARATUS FOR PROCESSING
A SEMICONDUCTOR AND
APPARATUS FOR PROCESSING
SEMICONDUCTOR DEVICE



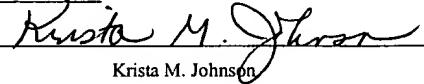
Group Art Unit: 2822

Examiner: Mike Lebentritt

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

4-21-99


Krista M. Johnson

IDENTIFICATION OF CO-PENDING APPLICATIONS

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

The attention of the Patent Office is drawn to the following co-pending applications:

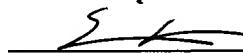
<u>Serial No.</u>	<u>Filing Date</u>	<u>Our Ref.</u>
09/236,620	01/26/99	0756-1931
08/962,841	11/03/97	0756-1753
08/691,434	06/03/96	0756-1551
08/967,317	10/28/97	0756-1746
08/739,192	10/30/96	0756-1582
08/661,869	06/11/96	0756-1523
09/197,534	11/23/98	0756-1894
08/956,438	10/23/97	0756-1735
08/956,439	10/23/97	0756-1737
08/721,540	09/26/96	0756-1574
08/095,172	07/23/93	0756-0891
08/183,800	01/21/94	0756-0958
09/038,926	03/09/98	0756-1768

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which may contain technical subject matter related to that of the present invention. This identification is not to be construed as a waiver of secrecy as to the cited applications now or upon issuance of the present application as a patent. The Examiner is respectfully requested to consider the cited applications and the art cited therein during examination of the present application.

Respectfully submitted,

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Attorney Docket No.: 0756-1630

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